



SRI SHRIDEVI CHARITABLE TRUST (R.)

SHRIDEVI INSTITUTE OF ENGINEERING AND TECHNOLOGY



NAME: FARAH BANU
Designation: Asst.Professor
Department: E&C

Date of Joining	19-09-2024		
Professional Experience	Teaching	Industry	Research

Contact Details	
Email ID	
LinkedIn	https://www.linkedin.com/in/farah-banu-b8175690
Google Scholar	
Research ID	
ORCID	
Vidwan -ID	

Academic Background
<ul style="list-style-type: none">Phd
<ul style="list-style-type: none">MTech (VLSI Design & Embedded Systems)
<ul style="list-style-type: none">BE (Electronics & Communication)

Areas of Interest	
<ul style="list-style-type: none">• Verilog	<ul style="list-style-type: none">• Basic Electronics
<ul style="list-style-type: none">• Digital Electronics	<ul style="list-style-type: none">• Embedded systems

Significant Publications
<ul style="list-style-type: none">No. of Papers Published in Journals (Numbers and Description) 01 IRJET1. Farah Banu, "An Area Efficient LTE Turbo Decoder Architecture", International research Journal of Engineering and Technology Volume:03 Issue:07/July-2016No. of Papers Presented in Conferences (Numbers and Description)Book Chapters (Numbers and Description)

- **List of FDP's/Workshops (Numbers and Description)**
- **Books (Numbers and Description)**
- **Patents Published (Numbers and Description)**
- **KSCST Student Projects Funded /Funded project and consultancy (Numbers and Information)**
- **Awards/Recognition**
- **Project Guided details**

Achievements

- **Editorial Board Member**
- **Nominated as**
- **Reviewer for**
- **Invited Speaker for**
- **Resource person for**
- **In-house resource person for**
- **Professional Membership**