



SRI SHRIDEVI CHARITABLE TRUST (R.)

# SHRIDEVI INSTITUTE OF ENGINEERING AND TECHNOLOGY



**NAME:** FARAH BANU  
**Designation:** Asst.Professor  
**Department:** E&C

<b>Date of Joining</b>	19-09-2024		
<b>Professional Experience</b>	Teaching	Industry	Research

Contact Details	
<b>Email ID</b>	farahbanu786@gmail.com
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<b>LinkedIn</b>	<a href="https://www.linkedin.com/in/farah-banu-b8175690">https://www.linkedin.com/in/farah-banu-b8175690</a>
<b>Google Scholar</b>	
<b>Research ID</b>	
<b>ORCID</b>	
<b>Vidwan -ID</b>	

Academic Background
<ul style="list-style-type: none"><li>Phd</li></ul>
<ul style="list-style-type: none"><li>MTech (VLSI Design &amp; Embedded Systems)</li></ul>
<ul style="list-style-type: none"><li>BE (Electronics &amp; Communication)</li></ul>

Areas of Interest	
<ul style="list-style-type: none"><li>Verilog</li></ul>	<ul style="list-style-type: none"><li>Basic Electronics</li></ul>
<ul style="list-style-type: none"><li>Digital Electronics</li></ul>	<ul style="list-style-type: none"><li>Embedded systems</li></ul>

Significant Publications
<ul style="list-style-type: none"><li><b>No. of Papers Published in Journals (Numbers and Description) 01 IRJET</b><ol style="list-style-type: none"><li>Farah Banu, "An Area Efficient LTE Turbo Decoder Architecture", International research Journal of Engineering and Technology Volume:03 Issue:07/July-2016</li></ol></li><li><b>No. of Papers Presented in Conferences (Numbers and Description)</b></li><li><b>Book Chapters (Numbers and Description)</b></li></ul>

- **List of FDP's/Workshops (Numbers and Description)**
- **Books (Numbers and Description)**
- **Patents Published (Numbers and Description)**
- **KSCST Student Projects Funded /Funded project and consultancy (Numbers and Information)**
- **Awards/Recognition**
- **Project Guided details**

## Achievements

- **Editorial Board Member**
- **Nominated as**
- **Reviewer for**
- **Invited Speaker for**
- **Resource person for**
- **In-house resource person for**
- **Professional Membership**